

Method And Device For Monitoring

Background of the Invention

5 (a). Field of the Invention

The present invention relates to a method and a device for monitoring, and more particularly, to a monitoring method and device capable of inspecting and controlling components inside a computer externally

(b). Description of the Prior Arts

10 Conventionally, when a computer is being used, there is no way for the user to know what is going on inside the computer. In order to provide users with more information about the operation statuses of components inside the computer, a monitor card is usually being used in the computer of prior arts for monitoring computer status, such as voltage abnormal of power source, 15 processor overheating, etc., and showing the corresponding messages detected using guard lamps or display device (e.g. display panel) such that the users can acquire the information and react accordingly.

20 In the R.O.C Appl. No. 89123784, a monitoring device 30 (as shown in Fig. 1 of the present invention, also is referred in the aforesaid R.O.C. patent as Fig.4) arranged in the recess located on a standard computer panel is provided. However, the foregoing monitoring device 30 can only detect the 25 temperature inside the shell of the computer 32, and has no way to detect the operation statuses of the components inside the computer 32, not to mention to be able to regulate the defected operation statuses externally. In this regard, the present invention provides a monitoring method and device for improving the aforesaid shortcomings such that a user can detect the operation statuses of components inside a computer from outside, and further can regulate the operation statuses directly by an external panel.

The primary object of the present invention is to provide a monitoring device and method for inspecting the operation statuses of components arranged inside the computer externally.

The monitoring device comprises: a detection unit, an I²C bus, a display panel, a processor, and a regulation unit. The detection unit is used for detecting and acquiring the information of operation statuses of components inside the computer. The I²C bus coupled to the detection unit is used for transmitting the forgoing detected information. The display panel arranged at a shell of the computer is used for displaying the foregoing information. The processor will receive the information from the I²C bus and thereby display the received information on the display panel. The regulation unit will issue a first control signal to the processor for requiring the processor to regulate the operation status of a component arranged inside the computer, and the processor will issue a second control signal to the detection unit through the I²C bus according to the first control signal so as to regulate the operation status of a component arranged inside the computer.

The monitoring method comprises the steps of:

20 using a detection unit for detecting and acquiring the information of operation statuses of components inside a computer;

25 using an I²C bus for transmitting the forgoing detected information to a display panel arranged at a shell of the computer;

20 using a regulation unit arranged at the display panel to issue a control signal to the detection unit through the I²C bus for regulating the operation status of a component inside the computer;

30 using the regulation unit to define a critical range of the aforesaid information;

evaluating whether the acquired information had exceeded the critical range and generating an alert signal shown on the display panel if the acquired information exceed the critical range.

Other and further features, advantages and benefits of the invention will become apparent in the following description taken in conjunction with

the following drawings. It is to be understood that the foregoing general description and following detailed description are exemplary and explanatory but are not to be restrictive of the invention. The accompanying drawings are incorporated in and constitute a part of this application and, 5 together with the description, serve to explain the principles of the invention in general terms. Like numerals refer to like parts throughout the disclosure.

Brief Description of the Drawings

10 The objects, spirits and advantages of the preferred embodiments of the present invention will be readily understood by the accompanying drawings and detailed descriptions, wherein:

15 FIG. 1 is a drawing showing a monitoring device arranged in the recess of standard computer panel with reference to the R.O.C. Pat. 89123784.

FIG. 2 and FIG. 3 are respectively a schematic diagram and a 3-D diagram showing a monitoring device applied between a computer and a video graphic array (VGA) card in accordance to the present invention.

20 FIG. 4 is a diagram showing another preferred embodiment of the monitoring device according to the present invention.

FIG. 5 is a preferred embodiment depicting the monitoring method of the present invention.

25 FIG. 6 is another preferred embodiment depicting the monitoring method of the present invention.

Detailed Description of the Present Invention

30 The characteristic of the monitoring device of the present invention is that users can easily acquire operation statuses of components arranged inside a computer visually and regulate the operation statuses using a

regulation unit installed on a panel by an I²C bus to enable operation statuses of component arranged inside a computer to be displayed on the panel.

Please refer to Fig. 2 and Fig. 3, which are respectively a schematic diagram and a 3-D diagram showing a monitoring device applied between a computer and a video graphic array (VGA) card in accordance to the present invention. As shown, the monitoring device 10 comprises: a detection unit 11, an I²C bus 12, a display panel 13, a processor 14, and a regulation unit 15. The computer 20 has a shell 36, wherein the display panel 13 is arranged in the front of the shell 36 such that user is able to inspect the display panel 13 visually with ease. The VGA card 40 includes: a graphic processing unit (GPU) 41, a fan 42, a memory 43 and a detection unit 11, wherein, the temperature of the GPU 41 processing the intended graphic operations of the computer 20 will rise while operating in high frequency, thus, the fan 42 is installed in the GPU 41 for decreasing the temperature of the GPU 41; the detection unit 11 will detect the status of the GPU 41, such as the temperature of the GPU 41, the working voltage of the GPU 41, the working frequency of the GPU 41, the rotation speed of the fan 43, etc., and thereby store the detected information into the memory 43; the display panel 13 is a liquid crystal display panel capable of displaying the hardware status information of the VGA card 40 arranged inside the computer, such as the temperature of the GPU 41, the working voltage of the GPU 41, the working frequency of the GPU 41, and the rotation speed of the fan 42; the I²C bus 12 is coupled between the detection unit 11 and the processor 14; the processor 14 will fetch the information stored in the memory 43 through the I²C bus 12.

The regulation unit 15 arranged on the display panel 13 further comprises a knob that users can adjust the working frequency of the GPU 41 using the knob.

When the monitoring device 10 is activated, the regulation device will issue a control signal to the processor 14 which will define a critical range of temperature, working voltage, and working frequency of the GPU 41 through the I²C bus 12 after receiving the control signal, and further will detect the hardware status of the VGA card 40 and store the detected information into the memory 43. Therefore, the processor 14 will be able to

fetch an actual hardware status (e.g. actual temperature, actual working voltage, and actual working frequency of the GPU) and will generate an alert signal if the fetched actual hardware status exceeds the critical range. In a preferred embodiment of the present invention, the alert signal is 5 displayed on the display panel 13 along with an alarm that users can see or hear the alert signal, and in consequence detect the abnormal of the VGA card. For instance, when the GPU 41 is overheated, the detection unit 11 will show a overheating alert on the display panel 13 to remind the user, and therefore the user will manually raise the rotation speed of the fan 42 or the 10 rotation speed of the fan 42 will be raised automatically according to a predefine range of rotation speed so as to lower the temperature of the GPU 41.

In another preferred embodiment of the present invention, users can apply the knob of the regulation unit 15 for directly setting up the frequency 15 of the GPU 41 form external. When the knob is turned by a user, the regulation unit 15 will issue a first control signal to the processor 14, and after the first control signal is received by the processor 14, the processor 14 will issue and a second control signal to the detection unit 11 through the I²C bus 12 for regulating the frequency of the VGA card so as to achieve the 20 objective of over clocking.

In an embodiment of the present invention (as shown in Fig. 4), a computer installed with a TV tuner card is able to detect and store the information of TV channel and FM channel that are tuned and set by the TV tuner card into the memory 43 using the detection unit 11 arranged in the 25 VGA card, thus, the processor 14 can fetch the information stored in the memory 43 through the I²C bus 12 and display thereof on the display panel 13.

Please refer to Fig. 5, which is a preferred embodiment depicting the monitoring method of the present invention. The method is exercised using 30 the aforesaid monitoring device 10, comprising the steps of:

step 90: using the detection unit 11 for detecting hardware status information of the VGA card 40 arranged in the computer 20, such as the temperature of the GPU 41, the working voltage of the GPU 41, the working frequency of the GPU 41, the rotation speed of the

fan 42, and storing the detected information into the memory 43;
5 step 91: using the I²C bus 12 to fetch and display the forgoing detected information on a display panel 13;
step 92: using the regulation unit 15 to issue a control signal to the detection unit 11 through the I²C bus 12 for regulating the frequency of the GPU 41;
10 step 93: using the regulation unit 15 to define a critical range of the status information of the VGA card 40; and
step 94: evaluating whether the acquired information had exceeded the critical range and generating an alert signal to be shown on the display panel if the acquired information exceed the critical range.

wherein, the I²C bus 12 is a serial bus having two data lines, i.e. data line for system clock and data line for system data, that are employed as the bus for communicating data between ICs.

15 Please refer to Fig.6, which is another preferred embodiment depicting the monitoring method of the present invention. The method comprises the steps of:

20 step 80: utilizing the I²C bus 12, the monitoring device 10 defines a critical range, such as the temperature of the GPU 41, the working voltage of the GPU 41, the working frequency of the GPU 41, the rotation speed of the fan 42;
step 81: utilizing the I²C bus 12, the monitoring device 10 fetches the actual working voltage, working frequency, temperature of the GPU 41 detected by the detection unit;
25 step 82: evaluating whether the detected information had exceeded the critical range, and issuing an alert signal for displaying on the display panel if the critical range is exceeded;
step 83: reacting properly, the processor 14 according to the alert signal, for instance, when the working voltage of the GPU 41 is too high or too low, the processor 14 will control the detection unit 14 through the I²C bus 12 to regulate the working voltage; when the temperature of
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the GPU 41 is too high, the processor 14 will control the detection unit 14 through the I²C bus 12 to raise the rotation speed of the fan 42 so as to lower the temperature.

Cooperating a VGA card with the method of the present invention is
5 just one of the embodiments. The method of the present invention is capable
of cooperating with other apparatus, such a chip on the motherboard, other
kinds of hardware or firmware, or even software.

While the present invention has been shown and described with
reference to a preferred embodiment thereof, and in terms of the illustrative
10 drawings, it should be not considered as limited thereby. Various possible
modification, omission, and alterations could be conceived of by one skilled
in the art to the form and the content of any particular embodiment, without
departing from the scope and the sprit of the present invention.